

Fig. 1 (PRIOR ART)

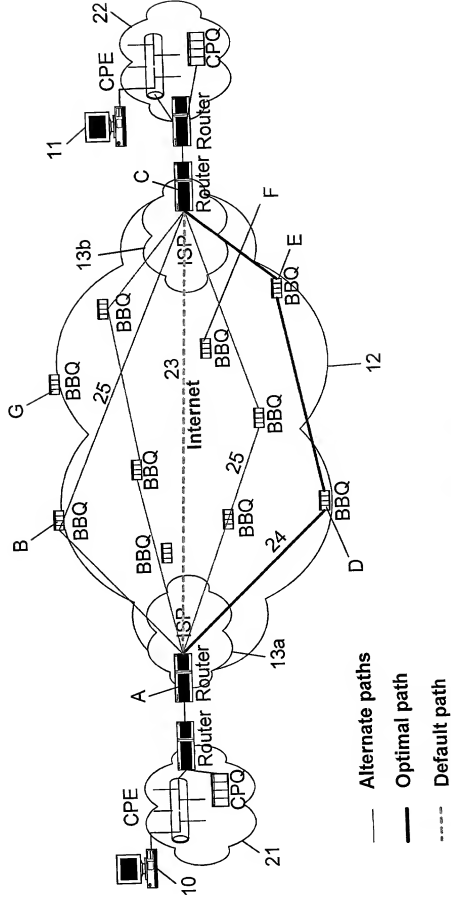


Fig. 2

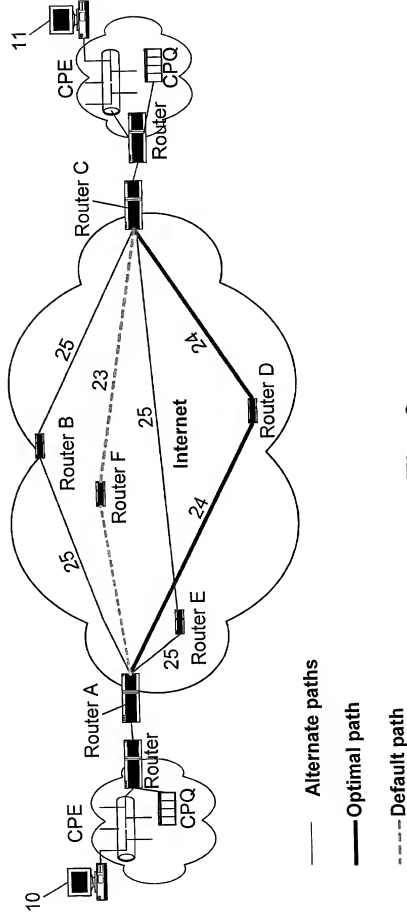


Fig. 3

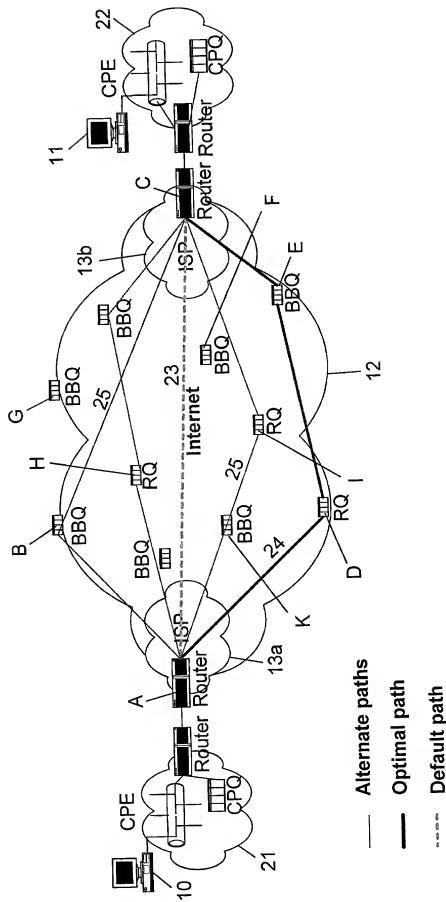


Fig. 4

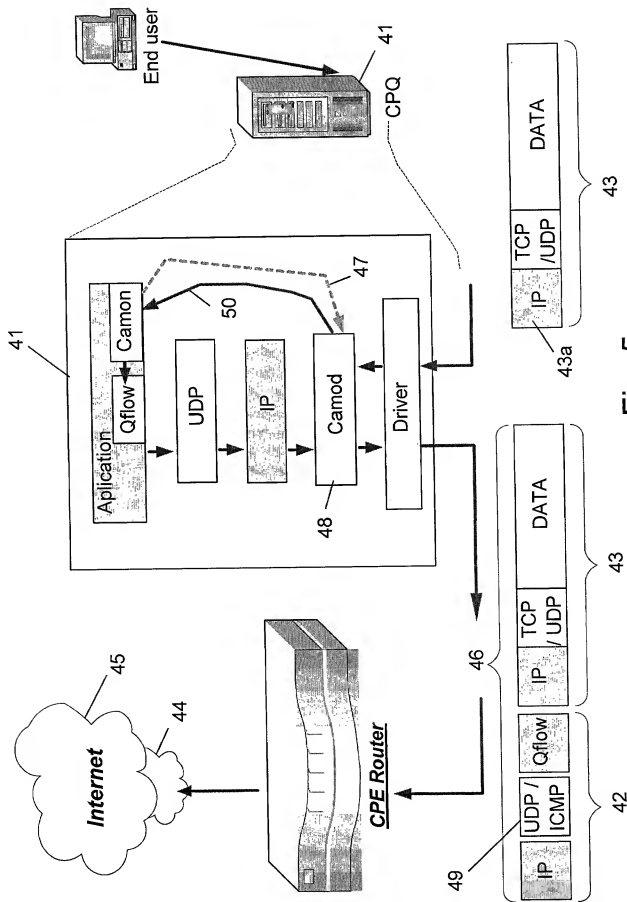


Fig. 5

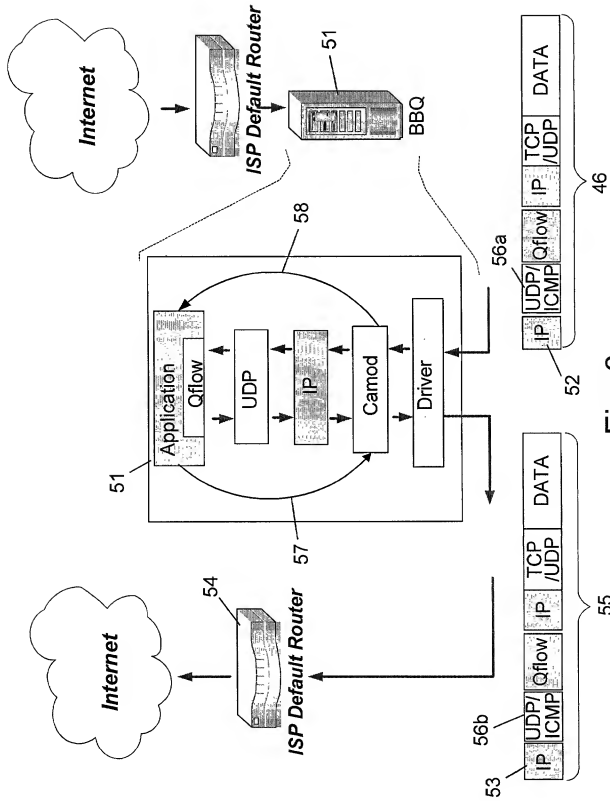


Fig. 6

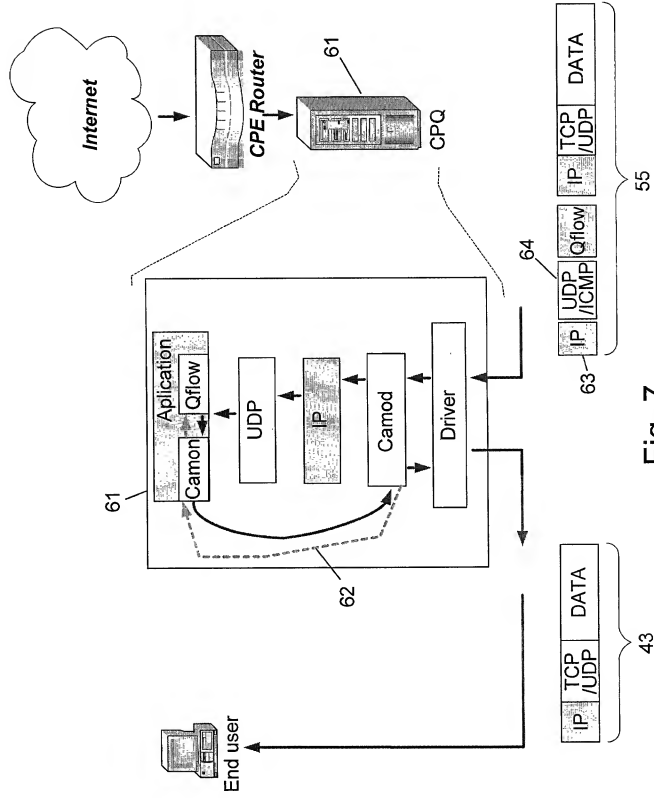


Fig. 7

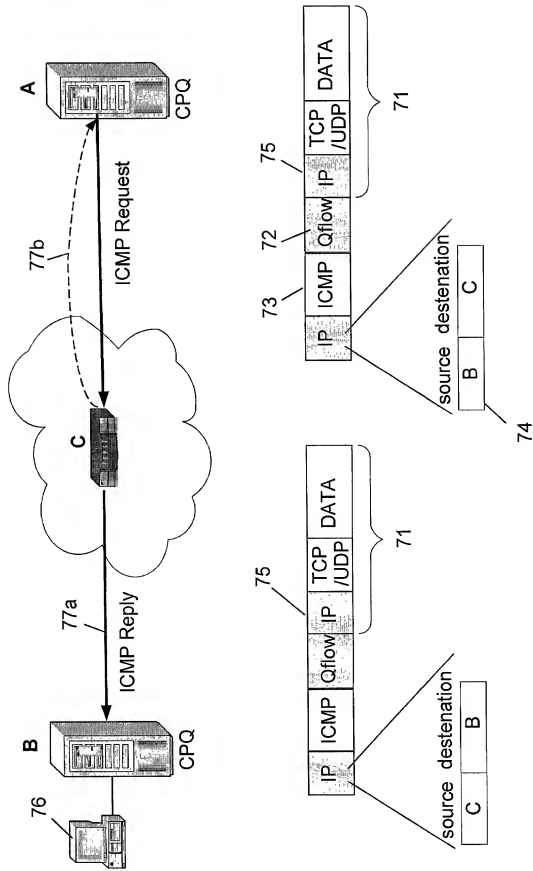


Fig. 8

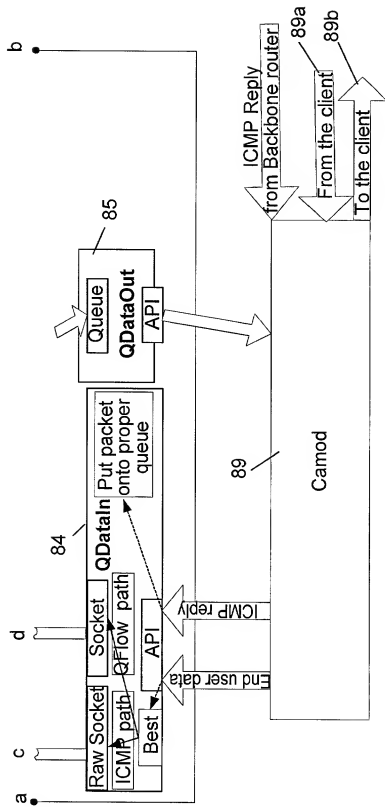


Fig. 9 (cont.)

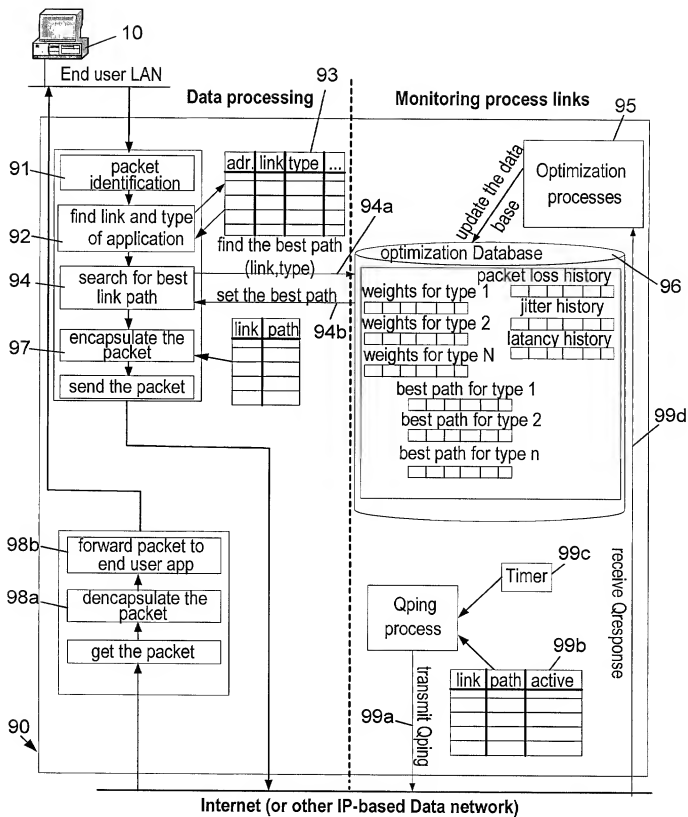


Fig. 10

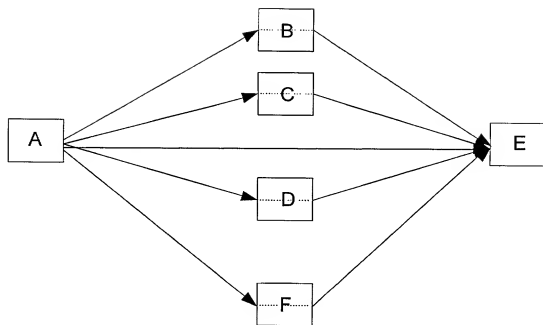


Fig. 11

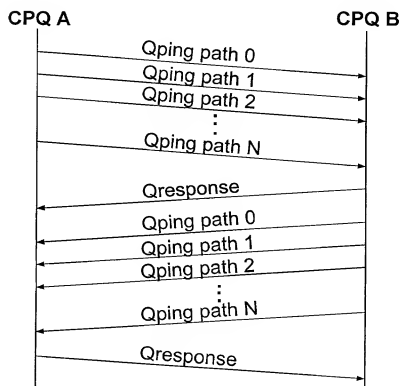


Fig. 12

bit no.	8	16	24
OP CODE	TOTAL LENGTH		HEADER LENGTH
QOS	ADDRESS TYPE MASK	NUMBER OF HOPS	HOPS OFFSET
HOP 1 ADDRESS			
HOP 2 ADDRESS			
.			
.			
.			
.			
HOP n ADDRESS			
ORIGINATOR ADDRESS			
TOTAL PATHS	PATH NUMBER	SESSION	
START TIME secs			
START TIME usecs			
OPTIONAL PADDING			

Fig. 13

bit no.	8	16	24
OP CODE	TOTAL LENGTH		HEADER LENGTH
QOS	ADDRESS TYPE MASK	NUMBER OF HOPS	HOPS OFFSET
HOP 1 ADDRESS			
HOP 2 ADDRESS			
.			
.			
.			
.			
HOP n ADDRESS			
ORIGINATOR ADDRESS			
TOTAL PATHS	RESERVED	SESSION	
PATH 1	DELTA TIME ms		EMPTY
PATH 2	DELTA TIME ms		EMPTY
.			
.			
.			
.			
PATH n	DELTA TIME ms		EMPTY

Fig. 14

10094500-20702
202002-00510001

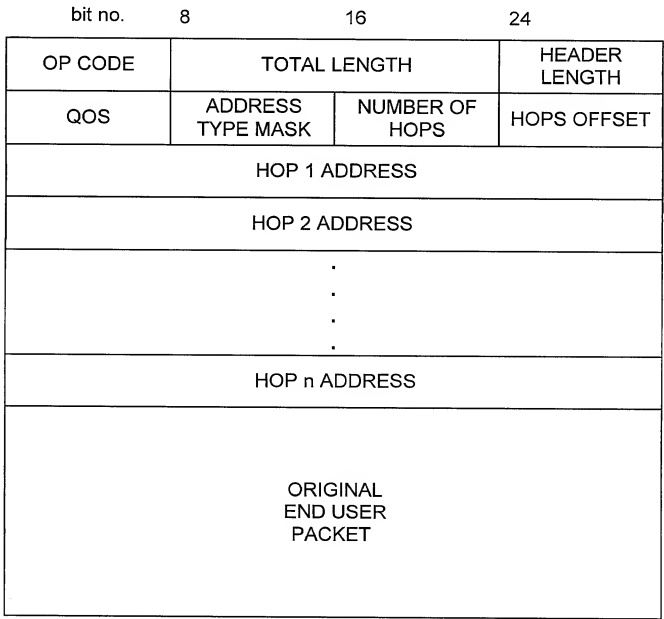


Fig. 15